

0 9 8 7 6 5 4 3 2 1

F

E

D

C

B

A

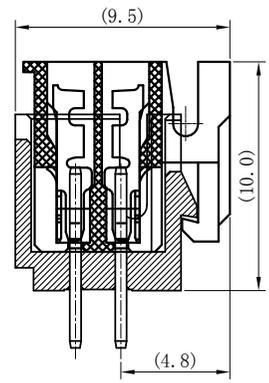
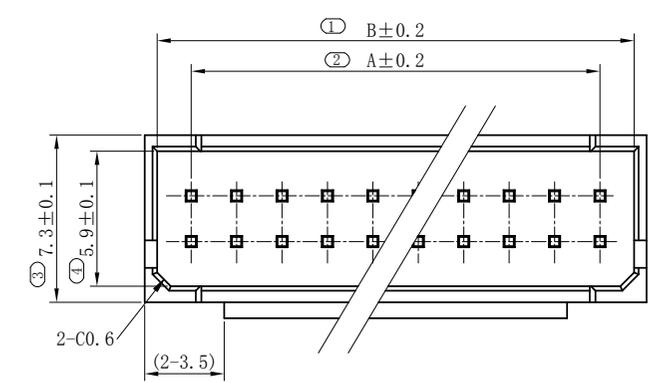
借用登记

旧底图总号

底图总号

签字

日期



嵌合状态

NOTES:

- MATERIAL SEE TABLE;
- PLATING SEE TABLE;
- RATING
- 3-1. OPERATING TEMPERATURE: -25~85°C
- 3-2. OPERATING VOLTAGE: 250V AC/DC
- 3-3. CURRENT RATING: 2.0 AMPS(24#)
- 3-4. DIELECTRIC WITHSTANDING VOLTAGE: 1000V AC/Minute;
- 3-5. INSULATION RESISTANCE: 1000MΩ Min;
- 3-6. CONTACT RESISTANCE: 0.03Ω Max;

PART NO. 61033-XX22-1001

SERIES

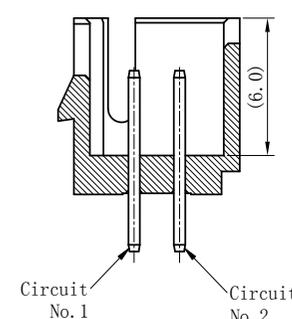
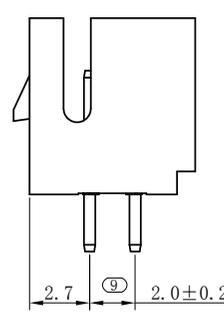
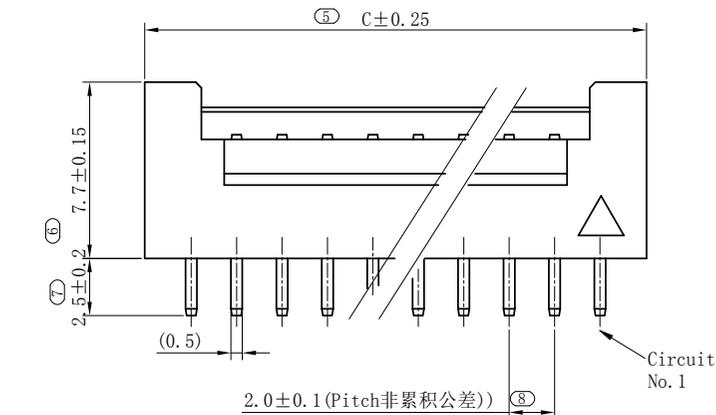
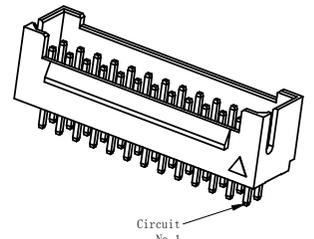
CIRCUIT NO. 4-22

Packing Option:
 2=Tray Packing
 3=Bag Packing

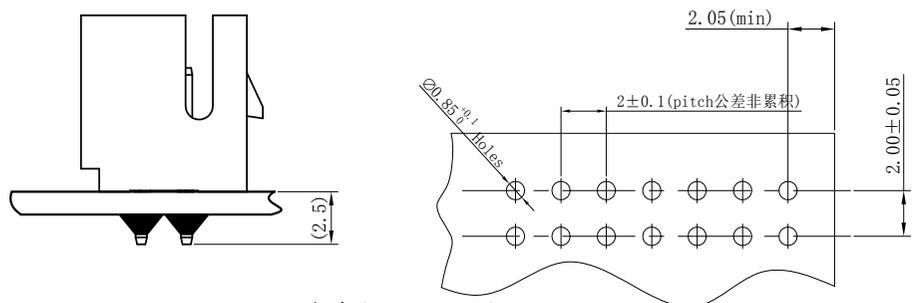
Lead Free & HF
 1&2=Lead Free
 5&6=Halogen-Free

Plating:
 2=MATTE-TIN
 80μ Min OVER Ni
 30μ Min.

REV.	DESCRIPTION	DATE
A/0	新版发行	2017.02.21



CKT	Dim A	Dim B	Dim C
8	6	8.85	9.95
10	8	10.85	11.95
12	10	12.85	13.95
14	12	14.85	15.95
16	14	16.85	17.95
18	16	18.85	19.95
20	18	20.85	21.95
22	20	22.85	23.95
24	22	24.85	25.95
26	24	26.85	27.95
28	26	28.85	29.95
30	28	30.85	31.95
32	30	32.85	33.95
34	32	34.85	35.95
36	34	36.85	37.95
38	36	38.85	39.95
40	38	40.85	41.95
44	42	44.85	45.95



PCB寸法(t=0.8-1.6)
P. C. BOARD Layout

序号	图号	名称	材料	数量	附注
3					
2	112-1033-002	鍍 Contact	鍍 Brass	n×1	电镀: 整体镀镍30u"MIN, 厚镀60u"MIN.
1	132-1033-001	鍍 Wafer	PA46(UL94V-0)	1	粘

GENERAL TOLERANCES		DRAWN BY	新富尔电子有限公司		
0.X	±0.25	赵博博/2017.02.21	XINFUER ELECTORN CO.,LTD		
0.XX	±0.12	崔华文/2017.02.21	SHEET		
0.XXX	±0.05	洪新江/2017.02.21	PROD. NAME PHD-2XnA-DK L=2.5		
UNIT	SIZE	SCALE	DRAWING NO.	1 OF 1	PART NO. 61033-**22-1001
MM	A4	1:1	006-1033-001	TITLE	
2.0 WTB CONN. DIP DUAL ROW VERTICAL					