

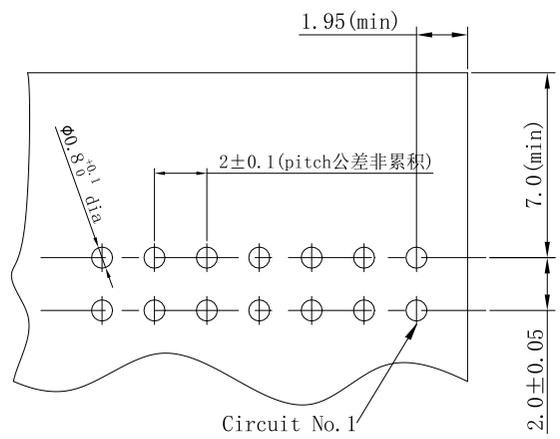
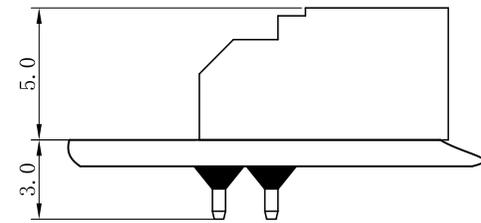
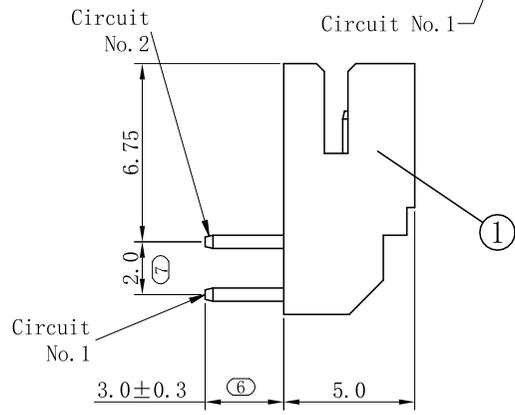
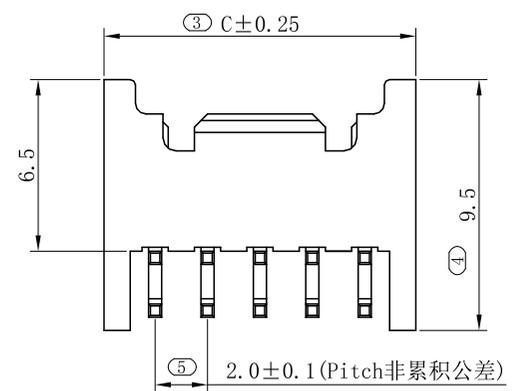
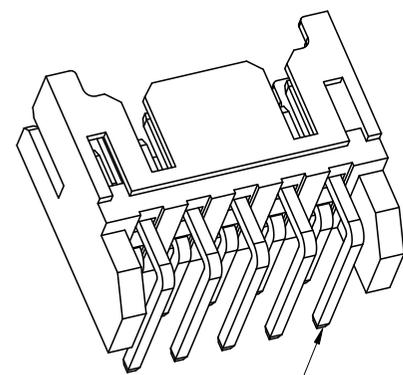
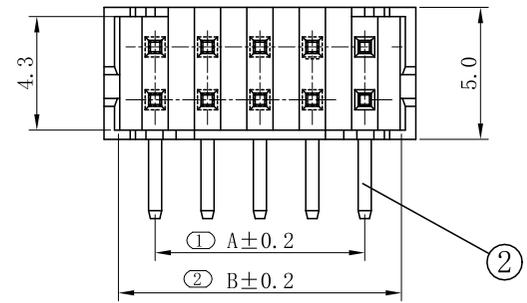
REV.	DESCRIPTION	DATE
A/0	新版发行	2017.02.18

- NOTES:
- MATERIAL SEE TABLE;
 - PLATING SEE TABLE;
 - RATING
 - OPERATING TEMPERATURE: -25~85°C
 - OPERATING VOLTAGE: 250V AC/DC
 - CURRENT RATING: 2.0 AMPS(24#)
 - DIELECTRIC WITHSTANDING VOLTAGE: 1000V AC/Minute;
 - INSULATION RESISTANCE: 1000M Ω Min;
 - CONTACT RESISTANCE: 0.03 Ω Max;
 - PART NO.

61032-XX22-1001
 SERIES
 CIRCUIT NO. 4-10
 Packing Option:
 2=Tray Packing
 3=Bag Packing

Lead Free & HF
 1=Lead Free

Plating:
 2=MATTE-TIN
 80 μ "Min OVER Ni
 30 μ "Min.



CKT	Dim A	Dim B	Dim C
4	2	4.85	5.95
6	4	6.85	7.95
8	6	8.85	9.95
10	8	10.85	11.95

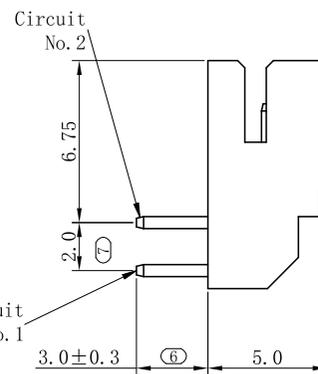
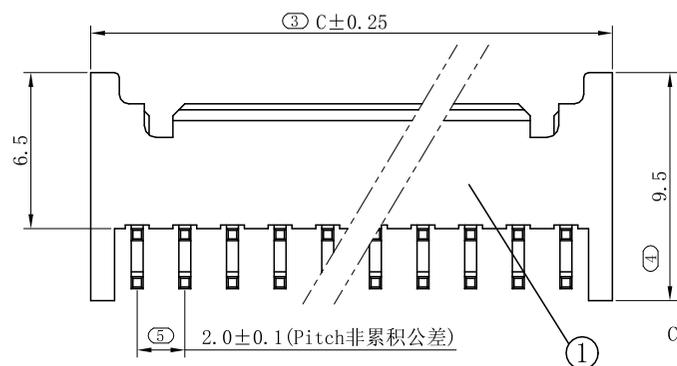
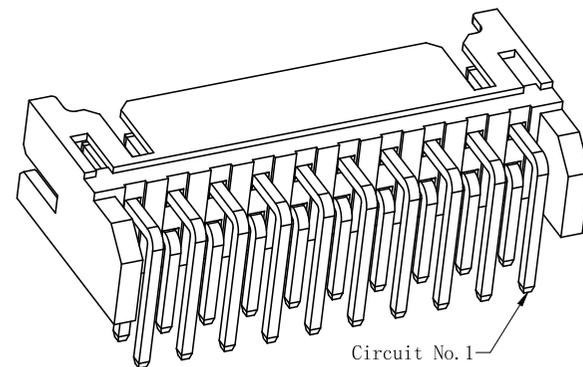
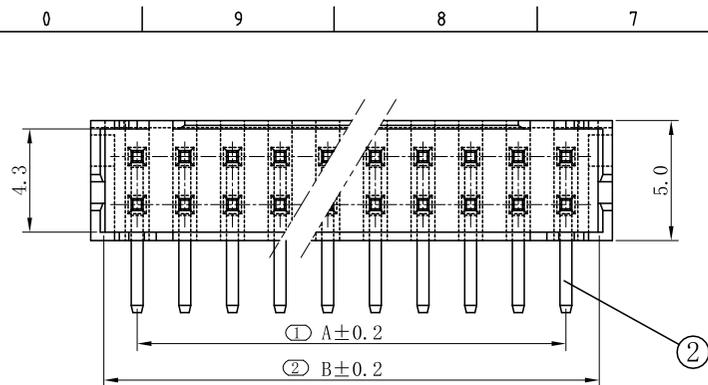
序号	图号	名称	材料	数量	附注
3					
2	112-1031-001	插针 Contact	黄铜 Brass	n x 1	电镀(锡): 整个表面镀底镍30 μ "MIN,再镀锡80 μ "MIN.
1	132-1031-001	基板 Wafer	PA66(IUL94V-0)	1	本色

GENERAL TOLERANCES			DRAWN BY	新富尔电子有限公司	
0.X	±0.25	X° ±3°	赵婷婷/2017.02.18	XINFUER ELECTORN CO.,LTD	
0.XX	±0.12		崔华文/2017.02.18		
0.XXX	±0.05	XX° ±0.5°	洪新江/2017.02.18		
UNIT	SIZE	SCALE	DRAWING NO.	SHEET	PROD. NAME
MM	A4	1:1	006-1032-001	1 OF 2	PHD-2 X nAW (2-5P)
				PART NO.	TITLE
				61032-**22-1001	2.0 WTB CONN. DIP DUAL ROW RIGHT-ANGLE

Circuit No. 1
 PCB寸法 (t=0.8-1.6)
 P.C. BOARD Layout

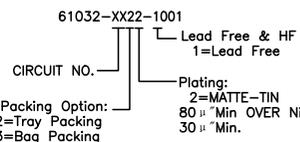
借用登记
 旧底图总号
 底图总号
 签字
 日期

REV.	DESCRIPTION	DATE
A/0	新版发行	2017.02.18

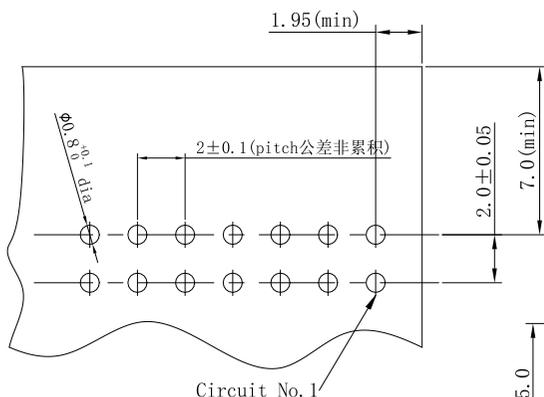


- NOTES:
1. MATERIAL SEE TABLE;
2. PLATING SEE TABLE;
3. RATING
3-1. OPERATING TEMPERATURE: $-25 \sim 85^{\circ}\text{C}$
3-2. OPERATING VOLTAGE: 250V AC/DC
3-3. CURRENT RATING: 2.0 AMPS(24#)
3-4. DIELECTRIC WITHSTANDING VOLTAGE: 1000V AC/Minute;
3-5. INSULATION RESISTANCE: 1000M Ω Min;
3-6. CONTACT RESISTANCE: 0.03 Ω Max;
4. PART NO.

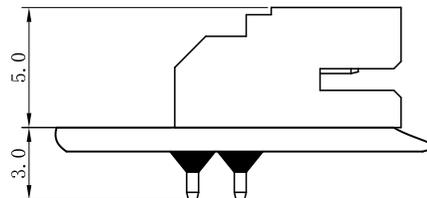
CKT	Dim A	Dim B	Dim C
12	10	12.85	13.95
14	12	14.85	15.95
16	14	16.85	17.95
18	16	18.85	19.95
20	18	20.85	21.95
22	20	22.85	23.95
24	22	24.85	25.95
26	24	26.85	27.95
28	26	28.85	29.95
30	28	30.85	31.95
32	30	32.85	33.95
34	32	34.85	35.95
36	34	36.85	37.95
38	36	38.85	39.95
40	38	40.85	41.95



Packing Option:
2=Tray Packing
3=Bag Packing



Circuit No. 1
P.C. BOARD Layout
P.C. BOARD Layout
($t=0.8-1.6$)



3					
2	112-1031-001	插针 Contact	黄铜 Brass	nx1	电镀(锡): 整个表面镀底锡30u"MIN,再镀锡80u"MIN.
1	132-1031-001	基板 Wafer	PA66(UL94V-0)	1	本色
序号	图号	名称	材料	数量	附注
GENERAL TOLERANCES		DRAWN BY	赵婷婷/2017.02.18		
0.X	± 0.25	X°	$\pm 3^{\circ}$	CHK. BY	崔华文/2017.02.18
0.XX	± 0.12			APRV. BY	洪新江/2017.02.18
0.XXX	± 0.05	XX°	$\pm 0.5^{\circ}$	SHEET	PROD. NAME PHD-2XnAW(6-20P)
UNIT	SIZE	SCALE	DRAWING NO.	2 OF 2	PART NO. 61032-**22-1001
MM	A4	1:1	006-1032-001	TITLE 2.0 WTB CONN. DIP DUAL ROW RIGHT-ANGLE	

借用登记
旧底图总号
底图总号
签字
日期